

**In the Title**

Please replace the title with the following:

-- METHOD AND APPARATUS FOR THE ELECTRICAL TESTING OF  
PRINTED CIRCUIT BOARDS EMPLOYING INTERMEDIATE LAYER  
GROUNDING --

**In the Specification**

Please amend the Specification, inserting before the current text:

This application is a continuation of co-pending US Patent Application Serial No. 09/719,753, filed February 27, 2001 and currently allowed, which is a US national phase application of PCT/IL99/00333, filed June 16, 1999, which claims priority from Israeli Patent Application Number 124961, filed June 16, 1998, from which the present application claims priority. Each of the foregoing applications is incorporated herein by reference.